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(54) Anisotropic silicon etching in fluorinated plasma.

(57) A method of high rate anisotropic etching of silicon in a high pressure plasma is described. The etching ambient is a mixture of either NF₃ or SF₆, N₂, and a polymerizing gas such as CHF₃ that creates conditions necessary for anisotropy not normally possible with nonpolymerizing fluorinated gases in a high pressure regime. The etch process is characterized by high etch rates and good uniformity utilizing photoresist or similar materials as a mask. In one embodiment the present process is used to etch deep trenches in silicon using a photoresist mask.

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ANISOTROPIC SILICON ETCHING IN FLUORINATED PLASMA

The present invention relates to device fabrication such as LSI or VLSI semiconductor chips, and more particularly to plasma etch processes designed to define microscopic patterns in such devices.

Many dry etching processes for etching silicon are known, typically involving plasmas in the so-called reactive ion etch (RIE) regime of relatively low pressure, approximately 30-100 microns, and low power density, about .01 to .5 watt/cm². Recently, much attention has been directed in the semiconductor industry to the plasma etching using high pressure, 1 mbar and above, and high power density, 2 to 10 watt/cm², resulting in substantially higher etch rates than possible using an RIE process.

In plasma etch processes, two removal components contribute to form the resulting etch profile in the target film: a chemical component, due to the chemical reaction of the plasma generated species with the surface material to be removed, and a physical component, due to the momentum transfer of the charged particles formed in the plasma and accelerated through the sheath to the target material. Plasma etch processes carried out in the high pressure regime are distinguished by the much greater importance of the chemical component in etching than in the low pressure RIE processes.

In the conventional fluorinated gas chemistry, as exemplified by US-A-4,310,380 to Flamm et al., etching is isotropic in nature, with comparable lateral and vertical etch rates in silicon. In the disclosed process the chemical component of the readily dissociated NF₃ ambient is very strong, even in the low pressure RIE type process, where one would normally expect a greater vertical etch rate than lateral rate due to the strength of the physical bombardment. In a high pressure regime, such a gas chemistry will become even more isotropic. While isotropic etching is useful in some silicon etch steps, it is not desirable where deep etching of silicon (3 to 5 microns) of small dimensions is required, such as in isolation trench etching. In such a process, a trench is etched around a transistor or other device which is then filled with a dielectric material to electrically isolate the device. The trench cuts vertically through several layers of differently doped polysilicon or silicon. An etch plasma which uses chlorinated gases to control undercut will undercut each layer a different amount depending on that layer's reactivity with fluorine and chlorine. These and other problems are overcome by the present invention.

One object of the present invention is to provide an improved plasma etching process for silicon, particularly when multiple silicon layers having different doping characteristics are present.

According to one embodiment of the invention, the etchant gas composition includes three major constituents: the etchant species, for example, NF₃ or SF₆; an inert gas, such as N₂; and a polymerizing gas such as CHF₃. Nitrogen trifluoride - (NF₃) readily dissociates in a plasma releasing free fluorine and fluorine containing radicals in greater quantities than alternate fluorine sources. It is also much safer than ClF₃, BrF₃, or IF₃ which are potentially explosive gases not suitable in a manufacturing environment. In fact, the extremely rapid dissociation of NF₃ in a high pressure plasma proves to cause rather nonuniform etching without the dilution by an inert gas. Nitrogen was found to yield some what better uniformities than argon or helium.

The addition of a small amount of a polymerizing gas to the high pressure plasma gives the present process its anisotropic character. The choice of polymerizing gases is determined by the type of mask used. A fluorine containing gas is preferred for photoresist, aluminum or chromium masks, while a silicon dioxide mask will necessitate in addition a chlorine containing gas to the fluorine etchant mixture. In the plasma, the gas will form a polymer which is subsequently conformally deposited on the target surface. In a vertical direction, the polymer is etched away, leaving a polymer passivated sidewall. The sidewall is protected from lateral etching and undercutting of the silicon is thus eliminated. In the particular case of trench isolation processing, the etchant species does not attack the sidewall of the differently doped polysilicon and so there is no variable under-cut such as that encountered in processes using chlorinated gases to control anisotropy.

FIG. 1 is a cross-sectional view in elevation of a high pressure single wafer reactor used to practice the process of present invention; and

FIG. 2 is a cross-sectional view of a portion of an integrated circuit device etched in accordance with the present invention, specifically, for trench isolation.

In accordance with the principles of the present invention, etching is performed in a high pressure, high plasma density single wafer reactor of the type shown in FIG. 1. The reactor is similar in design to that described in greater detail in the

copending U.S. patent application of Chen et al., Serial No. 623,670, filed June 22, 1984, and assigned to the present assignee, which is incorporated herein by reference.

Referring now to the drawings, there is shown in FIG. 1 a single wafer reactor 10, wherein a circular, electrically grounded upper electrode 11 is attached to a cylindrical housing 12. Housing 12 has a gas distributing baffle 13, a reactive gas inlet 14, and a cooling fluid inlet (not shown) and outlet 15. This assembly is contained within an insulating housing 16. The lower electrode 17 includes a conductive upper section 18 and an insulating lower section 19. Upper section 18 includes cooling channels 20 and a raised portion surrounded by an insulating ring 21 having gas exhaust channels 21a. The spacing 22 between the upper 11 and lower 17 electrodes is set at approximately 4 mm. An insulating ring 23, which electrically isolates the two electrodes, is formed of conduits 24 for exhausting the gas from the inter-electrode spacing. These conduits 24 open into a gap 25 between inner housing 16 and outer housing 26. The reacted gases are exhausted from the system through a port 27.

FIG. 2 is a greatly enlarged cross-sectional representation of a portion of a silicon wafer showing a trench etched according to the principles of the present invention. A patterned mask layer 28, for example, photoresist, aluminum, chromium or silicon dioxide, is formed on the surface of a heavily n-doped layer 30 of polycrystalline silicon. The mask layer 28, of course, must be resistant to the etching gas mixture. Layer 30 overlies a lightly p-doped monocrystalline silicon layer 32 which is formed on a silicon wafer 34. According to one embodiment of the present invention, the unmasked areas of layers 30 and 32 are etched anisotropically to form a trench 36 having substantially vertical sidewalls. Trench 36 may typically have a width of about 5 microns.

Fluorine containing polymerizing gases such as CHF_3 , C_2F_4 , C_2F_6 and C_3F_8 have been found to be advantageous in etching silicon or doped polysilicon through photoresist, aluminum or chromium mask layers. It has been found that a silicon dioxide mask layer requires, in addition, an amount of a chlorine containing gas such as CCl_4 , CFCI_3 , CF_2Cl_2 or C_2HCl_2 , the latter being a hydrogen containing freon.

It is to be understood that the specific sequence of doped or undoped layers are illustrative only, and that any sequence or number of layers of undoped polysilicon, doped polysilicon, and monocrystalline silicon may be etched utilizing the principles of this invention.

In accordance with a preferred embodiment of the present invention, a pressure of about 1 mbar is established in plasma reactor 10. The etchant gas is introduced at a total flow rate of about 24 $\text{s} \cdot \text{cm}^3$; 10 $\text{s} \cdot \text{cm}^3$ of NF_3 , 10 $\text{s} \cdot \text{cm}^3$ of N_2 , and 4 $\text{s} \cdot \text{cm}^3$ of CHF_3 . In the single wafer reactor, a power density of about 2 watts/cm² is generated at the wafer surface. The upper electrode 11 temperature is maintained at about 0 degrees C., and the temperature of the lower electrode at about -10 degrees C. The silicon wafer is partially masked and placed on the lower electrode 17. For the process conditions specified above, anisotropic etch rates of about 1.6 microns/min. have been observed.

The above example is illustrative only. More generally, etching can be carried out by selecting pressures, gas flows, and power densities in the ranges of 0.5 to 7 mbar, 10 to 100 $\text{s} \cdot \text{cm}^3$, and 1 to 10 watts/cm², respectively.

Claims

1. A method of etching a silicon body, comprising:

25 placing said silicon body in a gaseous plasma environment including a fluorine containing gaseous compound, an inert gas, and a polymer forming gaseous compound.

30 2. The method of claim 1, wherein said silicon body has a patterned masking layer thereon, and the etching of the silicon is anisotropic.

35 3. The method of claim 2, wherein said silicon body to be etched comprises multiple layers of differently doped polysilicon and monocrystalline silicon.

40 4. The method of claims 1 to 3, wherein said fluorine containing gas is selected from the group consisting of NF_3 and SF_6 , and said inert gas comprises nitrogen.

45 5. The method of claims 1 to 4, wherein said polymer forming gas is selected from the group consisting of hydrogen containing freons.

6. The method of claims 1 to 4, wherein said polymer forming gas is selected from the group consisting of CHF_3 , C_2F_4 , C_2F_6 and C_3F_8 .

55 7. The method of claims 1 to 6, wherein said gaseous plasma environment comprises a pressure of about 1 to 7 mbar and a power density of about 2 to 5 watts/cm² at the surface of said silicon body.

8. The method of claim 2, wherein said patterned masking layer comprises silicon dioxide and said polymer forming gaseous compound is selected from the group consisting of chlorine containing polymerizing gases.

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FIG. 1

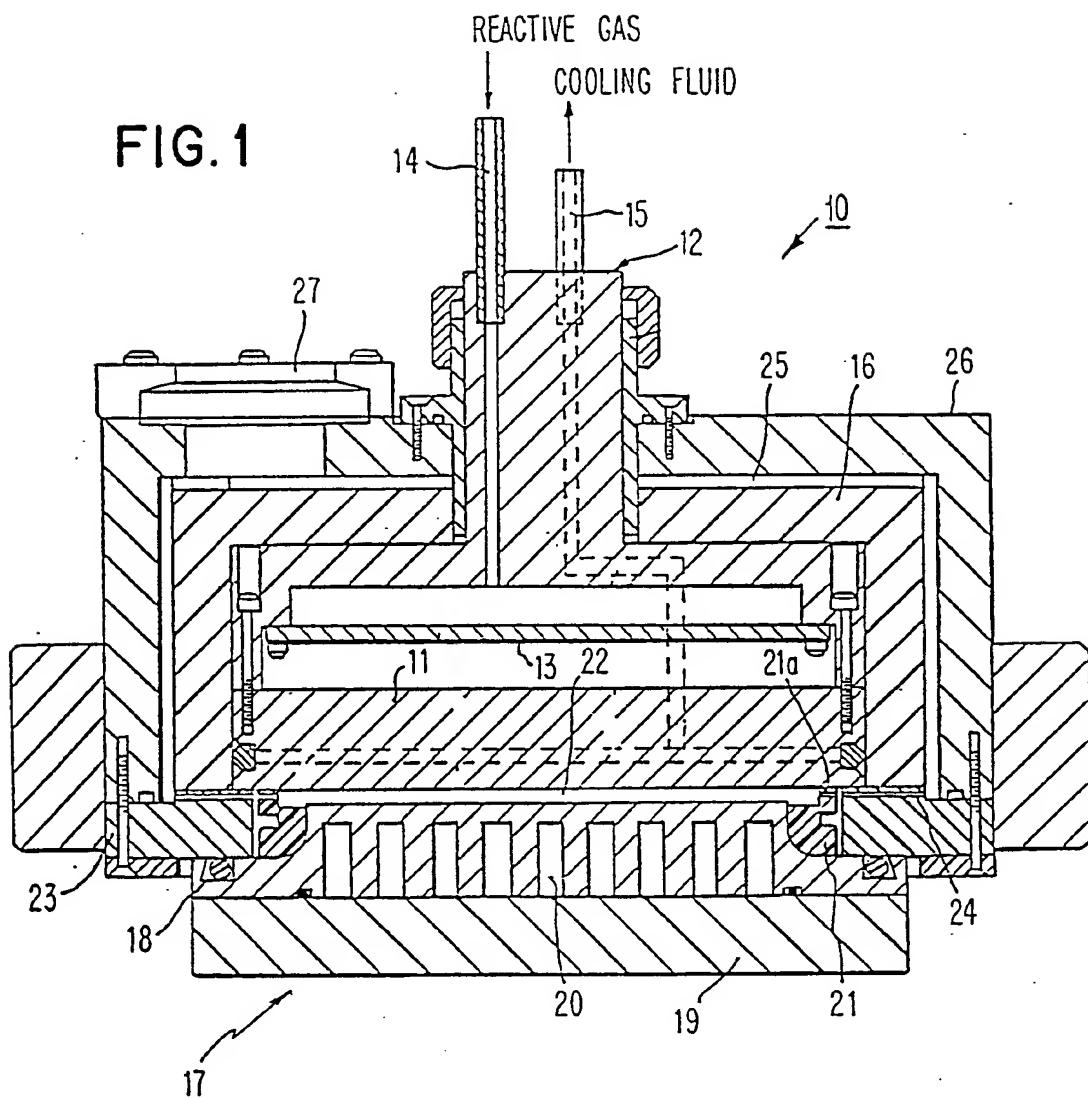


FIG. 2

